



Docket No.: 42P12542C

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re: the Application of:

PRIYADARSAN PATRA, ET AL.

Application No.: 10/665521

Filed: September 22, 2003

For: TIME SYNTHESIS FOR POWER OPTIMIZATION OF  
HIGH PERFORMANCE CIRCUITS

Art Group: 2825

Examiner: DO, Thuan V.

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

06/14/2006 FMETEKI1 00000014 10665521

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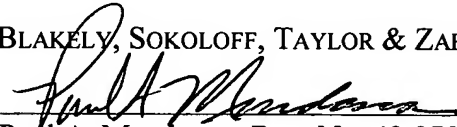
The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

The fee set in the amount of \$180.00 for submission of the Information Disclosure Statement is enclosed herewith. Please charge any additional fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: June 09, 2006

  
Paul A. Mendonsa, Reg. No. 42,879

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Los Angeles, CA 90025  
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Gayle Bekish

06-09-06

Date



# TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Application No.	10/665521
Filing Date	September 22, 2003
First Named Inventor	Priyadarsan Patra
Art Unit	2825
Examiner Name	DO, Thuan V.
Total Number of Pages in This Submission	7
Attorney Docket Number	42P12542C

## ENCLOSURES (check all that apply)

<input checked="" type="checkbox"/> Fee Transmittal Form  <input checked="" type="checkbox"/> Fee Attached  <input type="checkbox"/> Amendment / Response  <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s)  <input type="checkbox"/> Extension of Time Request  <input type="checkbox"/> Express Abandonment Request  <input checked="" type="checkbox"/> Information Disclosure Statement  <input checked="" type="checkbox"/> PTO/SB/08  <input type="checkbox"/> Certified Copy of Priority Document(s)  <input type="checkbox"/> Response to Missing Parts/Incomplete Application  <input type="checkbox"/> Basic Filing Fee <input type="checkbox"/> Declaration/POA  <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s)  <input type="checkbox"/> Licensing-related Papers  <input type="checkbox"/> Petition  <input type="checkbox"/> Petition to Convert a Provisional Application  <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address  <input type="checkbox"/> Terminal Disclaimer  <input type="checkbox"/> Request for Refund  <input type="checkbox"/> CD, Number of CD(s) <input type="checkbox"/> Landscape Table on CD	<input type="checkbox"/> After Allowance Communication to TC  <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences  <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)  <input type="checkbox"/> Proprietary Information  <input type="checkbox"/> Status Letter  <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): <div>Copies of 6 cited references Check for 180.00 Return Postcard</div>
Remarks		

## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Paul A. Mendonsa, Reg. No. 42,879 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Signature	
Date	June 9, 2006

## CERTIFICATE OF MAILING/TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Typed or printed name	Gayle Bekish		
Signature		Date	June 9, 2006



# TRANSMITTAL for FY 2005

Patent fees are subject to annual revision.

☐ Applicant claims small entity status. See 37 CFR 1.27.

TOTAL AMOUNT OF PAYMENT (\$)

180.00

## Complete if Known

Application Number	10/665521
Filing Date	September 22, 2003
First Named Inventor	Priyadarsan Patra
Examiner Name	DO, Thuan V.
Art Unit	2825
Attorney Docket No.	42P12542C

## METHOD OF PAYMENT (check all that apply)

☒ Check ☐ Credit card ☐ Money Order ☐ None ☐ Other (please identify): \_\_\_\_\_

☒ Deposit Account Deposit Account Number: 02-2666 Deposit Account Name: Blakely, Sokoloff, Taylor & Zafman LLP

For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)

☐ Charge fee(s) indicated below

☐ Charge fee(s) indicated below, except for the filing fee

☒ Charge any additional fee(s) or underpayment of fee(s) under 37 CFR §§ 1.16, 1.17, 1.18 and 1.20.

☒ Credit any overpayments

## FEE CALCULATION

### Large Entity Small Entity

Fee Code	Fee (\$)	Fee Code	Fee (\$)	Fee Description	Fee Paid
1051	130	2051	65	Surcharge - late filing fee or oath	
1052	50	2052	25	Surcharge - late provisional filing fee or cover sheet.	
2053	130	2053	130	Non-English specification	
1251	120	2251	60	Extension for reply within first month	
1252	450	2252	225	Extension for reply within second month	
1253	1,020	2253	510	Extension for reply within third month	
1254	1,590	2254	795	Extension for reply within fourth month	
1255	2,160	2255	1,080	Extension for reply within fifth month	
1401	500	2401	250	Notice of Appeal	
1402	500	2402	250	Filing a brief in support of an appeal	
1403	1,000	2403	500	Request for oral hearing	
1451	1,510	2451	1,510	Petition to institute a public use proceeding	
1460	130	2460	130	Petitions to the Commissioner	
1807	50	1807	50	Processing fee under 37 CFR 1.17(q)	
1806	180	1806	180	Submission of Information Disclosure Stmt	180.00
1809	790	1809	395	Filing a submission after final rejection (37 CFR § 1.129(a))	
1810	790	2810	395	For each additional invention to be examined (37 CFR § 1.129(b))	

Other fee (specify) \_\_\_\_\_

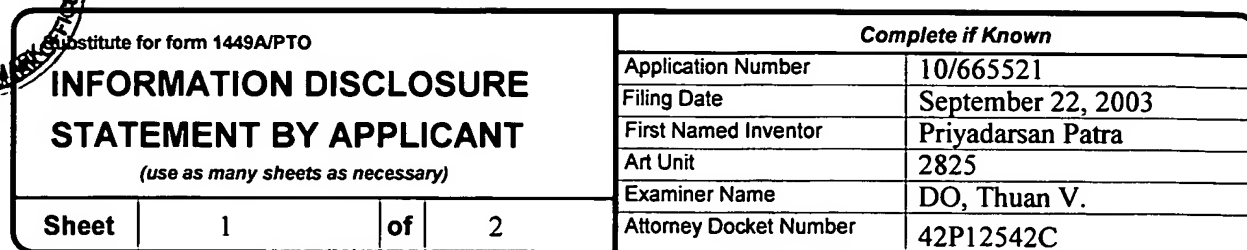
SUBTOTAL (2) (\$)

180.00

## SUBMITTED BY

## Complete (if applicable)

Name (Print/Type)	Paul A. Mendonsa	Registration No. (Attorney/Agent)	42,879	Telephone	(503) 439-8778
Signature		Date	06/09/06		

[illegible]

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Substitute for form 1449A/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Application Number	10/665521
				Filing Date	September 22, 2003
				First Named Inventor	Priyadarsan Patra
				Art Unit	2825
				Examiner Name	DO, Thuan V.
				Attorney Docket Number	42P12542C
Sheet	2	of	2		

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>	
		Chappell, Barbara, "The Fine Art of IC Design", IEEE Spectrum, Vol. 36, Issue 7, pp. 30-34 (July 1999).		
		Chappell, B., et al., "A System-Level Solution to Domino Synthesis with 2 GHz Application", Proceedings of the 2002 IEEE International Conference on Computer Design: VLSI in Computers and Processors (ICCD'02), pp. 164-171 (Sep. 16-18, 2002).		
		Conn, Andrew R., et al., "Circuit Optimization via Adjoint Lagrangians", 1997 IEEE/ACM International Conference on Computer-Aided Design, pp. 281-288 (Nov. 9-13, 1997).		
		Conn, Andrew R., et al., "Noise Considerations in Circuit Optimization", 1998 IEEE/ACM International Conference on Computer-Aided Design, pp. 220-227 (Nov. 8-12, 1998).		
		Shyu, Jyuo-Min, et al., "ECSTASY: A New Environment for IC Design Optimization", IEEE International Conference on Computer-Aided Design, pp. 484-487 (Nov. 7-10, 1988).		
		Visweswariah, Chandu, et al., "Noise Considerations in Circuit Optimization", IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 19, No. 6, pp. 679-690 (Jun. 2000).		

Examiner Signature		Date Considered	
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

<sup>1</sup>Applicant's unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.  
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